## Product Preview

# **SWITCHMODE Schottky Power Rectifier**

The SWITCHMODE Power Rectifier employs the Schottky Barrier principle in a large area metal-to-silicon power diode. State-of-the-art geometry features epitaxial construction with oxide passivation and metal overlay contact. Ideally suited for use as rectifiers in very low-voltage, high-frequency switching power supplies, free wheeling diodes and polarity protection diodes.

#### **Features**

- Highly Stable Oxide Passivated Junction
- Very Low Forward Voltage Drop
- Matched Dual Die Construction
- High Junction Temperature Capability
- High dv/dt Capability
- Guardring for Stress Protection
- Epoxy Meets UL 94 V-0 @ 0.125 in
- Electrically Isolated. No Isolation Hardware Required.
- This is a Pb-Free Device

#### **Mechanical Characteristics:**

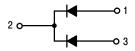
- Case: Epoxy, Molded
- Weight: 1.9 Grams (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Max. for 10 Seconds

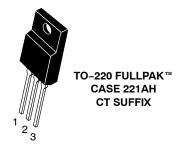
This document contains information on a product under development. ON Semiconductor reserves the right to change or discontinue this product without notice.

#### ON Semiconductor®

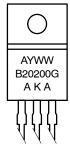
http://onsemi.com

## SCHOTTKY BARRIER RECTIFIER 20 AMPERES, 200 VOLTS





#### **MARKING DIAGRAM**



A = Assembly Location

Y = Year

WW = Work Week

B20200 = Device Code

G = Pb-Free Package

AKA = Polarity Designator

#### ORDERING INFORMATION

Device	Package	Shipping
MBRJ20200CTG	TO-220 (Pb-Free)	50 Units/Rail

<sup>\*</sup>For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON

#### MAXIMUM RATINGS (Per Leg)

Rating		Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	200	V
Average Rectified Forward Current (Rated V <sub>R</sub> ) T <sub>C</sub> = 125°C	Per Leg Per Package	I <sub>F(AV)</sub>	10 20	Α
Peak Repetitive Forward Current, Per Leg	(Rated V <sub>R</sub> , Square Wave, 20 kHz) T <sub>C</sub> = 90°C	I <sub>FRM</sub>	20	Α
Nonrepetitive Peak Surge Current (Surge applied at rated load conditions halfwave, single phase, 60 Hz)		I <sub>FSM</sub>	150	Α
Peak Repetitive Reverse Surge Current (2.0 μs, 1.0 kHz)		I <sub>RRM</sub>	1.0	Α
Operating Junction Temperature and Storage Temperature		T <sub>J</sub> , T <sub>stg</sub>	-65 to +150	°C
Voltage Rate of Change (Rated V <sub>R</sub> )		dv/dt	10,000	V/μs

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

#### THERMAL CHARACTERISTICS (Per Leg)

Rating		Symbol	Value	Unit
Thermal Resistance	Junction-to-Case	$R_{ heta JC}$	3.3	°C/W
	Junction-to-Ambient	$R_{\theta JA}$	105	

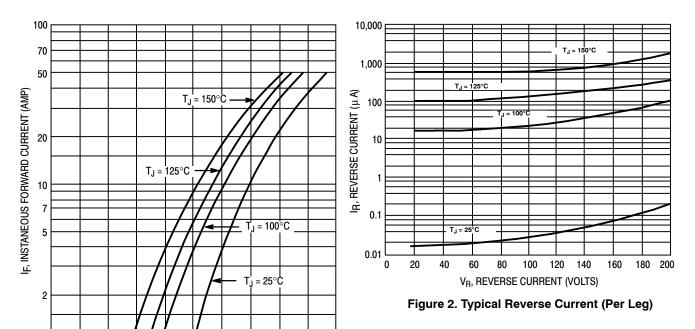
#### **ELECTRICAL CHARACTERISTICS** (Per Leg)

Rating	Symbol	Max	Unit
Maximum Instantaneous Forward Voltage (Note 1)	VF		V
$(i_F = 10 \text{ Amp}, T_C = 25^{\circ}C)$		0.9	
(i <sub>F</sub> = 10 Amp, T <sub>C</sub> = 125°C)		0.8	
(i <sub>F</sub> = 20 Amp, T <sub>C</sub> = 25°C)		1.0	
$(i_F = 20 \text{ Amp}, T_C = 125^{\circ}C)$		0.9	
Maximum Instantaneous Reverse Current (Note 1)	i <sub>R</sub>		mA
(Rated dc Voltage, T <sub>C</sub> = 25°C)		1.0	
(Rated dc Voltage, T <sub>C</sub> = 125°C)		50	

#### **DYNAMIC CHARACTERISTICS** (Per Leg)

Capacitance (V <sub>R</sub> = -5.0 V, T <sub>C</sub> = 25°C, Freq. = 1.0 MHz)	C <sub>T</sub>	500	pF

<sup>1.</sup> Pulse Test: Pulse Width = 300  $\mu s, \, Duty \, Cycle \leq 2.0\%$ 



 $\begin{array}{ccc} 0.4 & 0.6 & 0.8 \\ v_{F} \text{ INSTANTANEOUS VOLTAGE (VOLTS)} \end{array}$ Figure 1. Typical Forward Voltage (Per Leg)

0.2

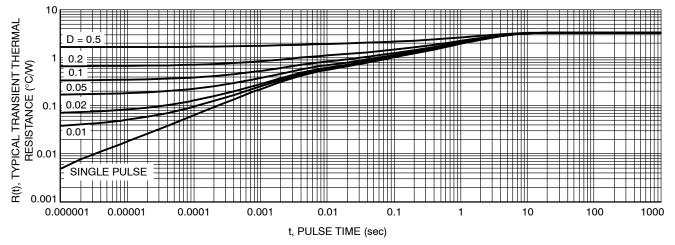


Figure 3. Typical Transient Thermal Response, Junction-to-Case

### **TEST CONDITION FOR ISOLATION TEST\***

FULLY ISOLATED PACKAGE

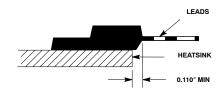
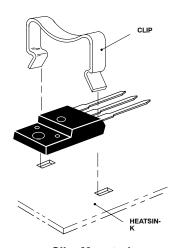


Figure 4. Mounting Position

\*Measurement made between leads and heatsink with all leads shorted together.

### **MOUNTING INFORMATION**

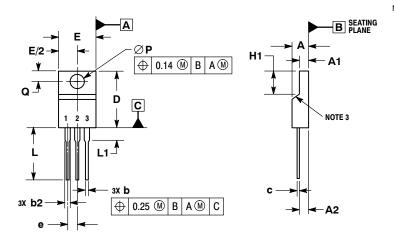


Clip-Mounted

Figure 5. Typical Mounting Technique

#### PACKAGE DIMENSIONS

#### TO-220 FULLPACK, 3-LEAD CASE 221AH ISSUE C



#### NOTES

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
  2. CONTROLLING DIMENSION: MILLIMETERS.

- 3. CONTOUR UNCONTROLLED IN THIS AREA.
  4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH AND GATE PROTRUSIONS. MOLD FLASH AND GATE PROTRUSIONS NOT TO EXCEED 0.13 PER SIDE. THESE DIMENSIONS ARE TO BE MEASURED AT OUTERMOST EXTREME OF THE PLASTIC BODY.
  DIMENSION b2 DOES NOT INCLUDE DAMBAR
- PROTRUSION. LEAD WIDTH INCLUDING PROTRUSION SHALL NOT EXCEED 2.00.

	MILLIMETERS		
DIM	MIN	MAX	
Α	4.30	4.70	
A1	2.50	2.90	
A2	2.50	2.70	
b	0.54	0.84	
b2	1.10	1.40	
u	0.49	0.79	
D	14.70	15.30	
Е	9.70	10.30	
е	2.54 BSC		
H1	6.70	7.10	
L	12.70	14.73	
L1		2.80	
Р	3.00	3.40	
Q	2.80	3.20	

FULLPAK is a trademark of Semiconductor Components Industries, LLC.

ON Semiconductor and war registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implications to be below or other applications. surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

#### **PUBLICATION ORDERING INFORMATION**

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA **Phone**: 303–675–2175 or 800–344–3860 Toll Free USA/Canada **Fax**: 303–675–2176 or 800–344–3867 Toll Free USA/Canada

Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

Japan Customer Focus Center Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative